

E155 Status Report

Team: _____

Project: _____

Report	_____ / 2
Schematics of Breadboard	_____ / 2
Block diagram of FPGA logic	_____ / 2
Microcontroller Routines	_____ / 2
Functioning hardware demo	_____ / 4
Well-written, mostly complete Verilog or code	_____ / 4
Completed riskiest element, > 50% done	_____ / 4
Total	_____ / 20